

GOVERNMENT POLYTECHNIC COLLEGE VALSAD Electrical Engineering Department EXPERT LECTURE REPORT



On April 18, 2024, Professor Devendra Tandel delivered an expert lecture on "Digital Logic Circuit Design using Proteus Software" for 4th-semester students. Currently serving as an Assistant Professor at Government Engineering College, Valsad, Professor Devendra Tandel facilitated the event.

The objective of the expert lecture was to familiarize students with the utilization of simulation tools for designing and testing digital logic circuits.

The lecture took place in the Civil seminar hall, where 63 second-year students (SEM 4) from the Electrical Engineering department participated. Professor Tandel meticulously guided the students through the process of designing and testing digital logic circuits using Proteus Software. He elaborated on the step-by-step procedure, starting from the fundamentals of basic logic gates to the simulation of complex combinational and sequential circuits.

Throughout the session, Professor Tandel effectively correlated different topics, ensuring an engaging learning experience for the students. He emphasized the significance of simulation as a precursor to hardware implementation, showcasing simulations of Half Adders, Encoders, Decoders, Multiplexers and Counters.

The organization and coordination of the expert lecture were managed by Mr. K.H. Tandel, Lecturer in EC Engineering, and Mr. H.N. Lad, Lecturer in Electrical Engineering.

Feedback from the students reflected their satisfaction with the session, underscoring the valuable insights gained into Digital Logic Circuit Design using Proteus Software.

Name of Course	Digital Electronics & Digital Instruments
Code	4340904
CO Covered	 CO2 - Use digital integrated circuit and logic family chips CO3 - Build sequential and combinational logic circuits.

Session Photos







GOVERNMENT POLYTECHNIC VALSAD ELECTRICAL ENGINEERING DEPARTMENT

EXPERT LECTURE ON: Digital logic circuit Design SEM: Ath using Proteus Software

DATE: 18/04/2024

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